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Improvements in, or Relating to, VDSL

The present invention relates to a method of using VDSL zipper duplex transmission in an unsynchronised mode, a system employing VDSL zipper duplex transmission, in an unsynchronised mode, over a common twisted pair cable, a transmission system employing the method, a receiver for use with the transmission system, a transmitter for use with the transmission system and a modem, incorporating a receiver and transmitter, for use with the transmission system.

In our co-pending patent application WO 9706619, there is described a procedure for suppressing near-end crosstalk (NEXT) when using duplex communication, in twisted pair wire networks, in which different sub-carriers are used in the two transmission directions, and in which the subcarriers are orthogonal. Preferably a number of sub-carriers are transmitted in each transmission direction. Alternate sub-carriers are used in each direction. The number of sub-carriers used in one transmission direction may be greater than the number used in the other transmission direction.

In our co-pending patent application, referenced above, the use of Zipper VDSL with time synchronization is described. The present invention complements the earlier invention, in that it enables VDSL Zipper duplex transmission, as described in our co-pending patent application, without time synchronization between different modem pairs over the same twisted copper pair cable, i.e. in the same binder group.

Zipper is a time-synchronized frequency division duplex implementation of discrete multi tone (DMT) modulation. Two communicating Zipper modems transmit DMT symbols simultaneously with a common clock. The Zipper scheme implies that every carrier, in the total set of carriers in the DMT signal, is exclusively chosen to be used for either for the up-stream, or the down-stream, direction. When all transmitters are time synchronized, the near end cross-talk (NEXT) and near end echoes, injected into the received signal, are orthogonal to the desired signal. To ensure the orthogonality between the signal and all the noise sources

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originating from DMT signals in the opposite direction, the guard time, with cyclic extension of the symbols between consecutive symbols, must be dimensioned for the maximum propagation delay of the channel. Further, the size of the guard time is minimised by applying timing advance.

5 Several duplex VDSL systems may share the same twisted pair copper cable. Such systems are referred to as belonging to the same binder group. VDSL systems, using Zipper, which belong to the same binder group, are affected by line attenuation, near end echo and crosstalk. In known Zipper transmission systems, when timing advance is used, all transceivers, in a binding group, start the
10 transmission of each frame at the same time.

There are three types of signal that affect the length of the cyclic extension in each frame:

- the received signal;
- the echo signal caused by imperfect balance of the hybrid and
15 impedance discontinuities in the line; and
- the NEXT signal.

Orthogonality, between the received signal, the echo signal and the NEXT signal, is preserved if each sampled DMT symbol is disturbed by a single frame from each one of the near-end transmitters. Because of this, in order to preserve orthogonality, the cyclic extension has to be dimensioned to cover all impulse
20 responses from the line, the echoes and the NEXT.

However, in certain situations it may be difficult, for practical reasons, or otherwise undesirable, to provide and maintain time synchronization between all transmitters in the same binder group. It is therefore desirable to enable Zipper modems, if necessary, to operate in a non-synchronized mode. In other words, only pairwise synchronisation is maintained between a VTU-O and VTU-R pair. The
25 present invention provides this option for Zipper modems and systems employing

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Zipper modems. Thus, the present invention permits different users to transmit time asynchronous DMT frames in the same binder group. However, although this is always possible, a noticeable performance penalty can be expected in many situations because of increased NEXT. One way in which the present invention mitigates increased NEXT is by pulse shaping of DMT frames prior to transmission and providing additional pulse shaping at the receiver. Using pulse shaping at the transmitter has the additional advantage of higher suppression of the side lobes of the DMT spectrum. This increases spectral compatibility with other systems, such as, ADSL and CAP-VDSL.

According to a first aspect of the present invention, there is provided a telecommunications transmission system using zipper having at least two VDSL systems, each comprising a pair of zipper modems, said at least two VDSL systems belonging to a binder group common to both VDSL systems, characterised in that said telecommunications transmission system is adapted to:

- handle zipper transmissions transmitted in said binder group;
- at least partly mitigate NEXT; and
- permit transmissions in a first VDSL system which are asynchronous with transmissions in at least a second VDSL system.

DMT frames may be pulse shaped prior to transmission.

Said pulse shaping may produces an improved suppression of side lobes of said DMT's spectrum.

A cyclic extension may be added to DMT symbols, said cyclic extension comprising:

- a suffix which is greater than, or equal to, a channel's propagation delay; and

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- a prefix which is greater than, or equal to, a guard time needed to eliminate inter-symbol interference.

Said pulse shaping may be produced by forming pulse shaped wings on a DMT frame in cyclic extensions of the DMT frame.

5 Said pulse shaped wings may be in the form of a raised cosine pulse.

Said pulse shaping may be performed at a transmitter, after addition of a cyclic prefix and cyclic suffix, to a symbol and prior to digital to analogue conversion.

A DMT signal received by a receiver may be windowed to further reduce NEXT.

10 Said windowing may be performed by:

- multiplying μ samples at the beginning and end of a block of $2N+\mu$ samples;
- folding and adding $\mu/2$ samples from the beginning of the $2N+\mu$ block of samples to the end of the $2N$ remaining samples; and
- 15 folding and adding $\mu/2$ samples from the end of the $2N+\mu$ block of samples to the beginning of the $2N$ remaining samples.

The same number of sub-carriers may be used for transmission in the up stream direction as are used for transmission in the down stream direction.

A different number of sub-carriers may be used for transmission in the up stream and down stream directions.

20 According to a second aspect of the present invention, there may be provided, in a telecommunications transmission system using zipper and having at least two VDSL systems, each comprising a pair of zipper modems, said at least

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two VDSL systems belonging to a single binder group common to both VDSL systems, a method of transmission characterised by permitting zipper transmissions of said first and, at least, said second VDSL transmission systems, to be transmitted in said single binder group, where transmission in said first VDSL system are asynchronous with zipper transmission in said second VDSL system and in which the effects of NEXT are, at least partly, mitigated.

DMT frames may be pulse shaped prior to transmission.

Said pulse shaping may produce an improved suppression of side lobes of said DMT's spectrum.

A cyclic extension may be added to DMT symbols, said cyclic extension comprising:

- a suffix which is greater than, or equal to, a channel's propagation delay; and
- a prefix which is greater than, or equal to, a guard time needed to eliminate inter-symbol interference.

Pulse shaped wings may be formed on a DMT frame in the cyclic extensions of the DMT frame.

Said pulse shaped wings may be formed as a raised cosine pulse.

Said pulse shaping may be performed at a transmitter after addition of a cyclic extension to a symbol and prior to digital to analogue conversion.

A DMT signal received by a receiver may be windowed to further reduce NEXT.

The following steps may be used to perform said windowing:

- multiplying μ samples at the beginning and end of a block of $2N+\mu$

samples;

- folding and adding $\mu/2$ samples from the beginning of the $2N+\mu$ block of samples to the end of the $2N$ remaining samples; and
- folding and adding $\mu/2$ samples from the end of the $2N+\mu$ block of samples to the beginning of the $2N$ remaining samples.

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The same number of sub-carriers may be transmitted in both an upstream and down stream direction.

A different number of sub-carriers transmitted in the up stream and down stream directions.

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According to a third aspect of the present invention, there is provided a transmitter, for use in a transmission system as set forth in any preceding paragraph, characterised in that said transmitter comprises an a b-bit buffer and encoder for receiving an input bit stream at a rate of R bit/s, a n-point IDFT processor for receiving an output from said b-bit buffer and encoder, extension means for adding a cyclic extension to an output of said IDFT processor, a pulse shaper for shaping a DMT symbol output from said extension means, and a digital to analogue converter and low pass filter for converting a DMT symbol received from said pulse shaper to analogue form and passing said DMT to a transmission channel.

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Said pulse shaper may form pulse shaped wings on said DMT symbol as raised cosine pulses.

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According to a fourth aspect of the present invention, there is provided a receiver, for use in a transmission system as set forth in any preceding paragraph, characterised in that said receiver includes an analogue to digital converter for digitising a DMT symbol received from a transmission channel, a windowing unit connected to an output of said analogue to digital converter, a stripper unit for removing cyclic extensions to said DMT symbol, an n-point DFT processor for

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receiving an output from said stripper unit, a frequency domain equalisation unit for receiving an output from said n-point DFT unit and decoder, and a b-bit buffer for receiving an output from said frequency domain equalisation unit and outputting a bit stream at R bit/s.

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Said windowing unit may perform the following operations:

- multiplying μ samples at the beginning and end of a block of $2N+\mu$ samples;
- folding and adding $\mu/2$ samples from the beginning of the $2N+\mu$ block of samples to the end of the $2N$ remaining samples; and
- 10 - folding and adding $\mu/2$ samples from the end of the $2N+\mu$ block of samples to the beginning of the $2N$ remaining samples.

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According to a fifth aspect of the present invention, there is provided a modem, for use in a transmission system as set forth in any preceding paragraph, characterised in that said modem includes a transmitter as set forth in any preceding paragraph.

Said modem may include a receiver, as set forth in any preceding paragraph.

Embodiments of the invention will now be described, by way of example, with reference to the accompanying drawings, in which:

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Figure 1 illustrates the generation of interference signals, cross-talk and echo, that affect the orthogonality of Zipper.

Figure 2 illustrates sampling, at the VTU-O side, of a received frame disturbed by frames from near end transmitters.

Figure 3 illustrates a DMT transmitter/receiver pair for down-stream

transmission, exemplifying symmetric communication where every second sub-carrier is used, respectively, for the up (u), and down-stream (d), directions.

Figure 4 illustrates the frame format for zipper.

Figure 5 illustrates non-orthogonal NEXT.

Figure 6 illustrates pulse shaping of the DMT frame.

Figure 7 is a schematic illustration of the pulse shaping process in the transmitter and the windowing process in the receiver.

Figure 8 illustrates windowing of a received DMT frame.

Figure 9 illustrates asynchronous non-orthogonal NEXT with and without windowing and pulse shaping.

To fully appreciate the operation of the present invention it is necessary to consider the invention disclosed in our earlier co-pending patent application WO 9706619. As previously explained, the present invention enables Zipper to be used in a manner in which VDSL systems operating in the same binder group can operate without mutual synchronisation between each system.

Zipper, as disclosed in our co-pending patent application WO 9706619, is a time-synchronized frequency-division duplex implementation of discrete multi tone (DMT) modulation. Two communicating Zipper modems transmit DMT symbols simultaneously with a common clock. The Zipper scheme implies that every carrier, in the total set of carriers in the DMT signal, is selected for use in either the up-stream, or the down-stream, direction. When all transmitters are time synchronized, the near end cross-talk (NEXT) and near end echoes injected into the received signal, are orthogonal to the desired signal. To ensure that orthogonality is maintained between the signal and all the noise sources, originating from DMT signals in the opposite direction, the guard time with cyclic extension of the symbols,

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between consecutive symbols, must be dimensioned for the maximum propagation delay of the channel. Further, the size of the guard time is minimized by applying timing advance.

Figure 1 depicts how two VDSL systems, sharing the same cable, i.e. in the same binder group, are affected by line attenuation, near end echo and cross-talk. Sources of interference that can be identified include:

- echo generated within the hybrids and passed from the transmitters to the receivers;
- NEXT which passes between the transmitter - receiver pairs at the near end; and
- FEXT which passes between the transmitter - receiver pairs at the far end and is transmitted over the full cable length.

When timing advance is used, all transceivers start the transmission of each frame at the same time. There are three types of signals that affect the length of the cyclic extension in each frame: the received signal, the echo-signal due to imperfect balance of the hybrid and impedance discontinuities in the line, and finally the NEXT signal. Figure 2 shows sampling at the VTU-O side of received frame disturbed by frames from near-end transmitters.

As can be seen from Figure 2, the orthogonality between the desired part of the received signal and the disturbances (interference signals) is preserved if each sampled DMT symbol is disturbed by a single frame from each one of the near-end transmitters. As a consequence, to preserve orthogonality, the cyclic extension has to be dimensioned to cover all impulse responses from the line, the echoes, and the NEXT.

The management of the capacity split between up-stream and down-stream is performed by assigning individual carriers for both directions. For example, if a fully symmetric service is required, sub-carriers with even indices can be assigned

to the up-stream and sub-carriers with odd indices can be assigned to the down-stream. Similarly, for an asymmetric 8:1 split, each ninth carrier can be assigned to the up-stream and the other carriers assigned to the down-stream. However, for the sake of spectral compatibility with other existing and future systems, operating on the same cable, alternative carrier assignments can be used.

The transmission and reception of symbols is performed simultaneously at both ends by the VTU-O and by the VTU-R. For down-stream transmission the bit stream is encoded by the VTU-O transmitter into a set of quadrature amplitude modulated (QAM) sub-symbols, where each QAM sub-symbol represents a number of bits determined by the signal-to-noise ratio (SNR) of its associated down-stream sub-channel, the desired overall error probability, and the target bit rate. The set of sub-symbols is then input, as a block, to a complex-to-real discrete Fourier transform (IDFT) processor, see Figure 3. Following the IDFT, a cyclic prefix is added to the output samples to eliminate intersymbol interference, and a cyclic suffix is appended to the output samples to maintain orthogonality between the desired signal and near-end distorting signals. The result is then converted from digital to analog format and applied to the channel.

Thus, the input bit stream, see Figure 3, enters a b-bit buffer and encoder, from which its is passed to the N-point IDFT processor and thence to the cyclic extension adder. Following which the signal is passed via the DAC and lowpass filter to the channel.

At the VTU-R receiver, after analog-to-digital conversion, the cyclic prefix and suffix are stripped, and the samples are transformed back to the frequency domain by a DFT. Each output value used for down-stream transmission is then scaled by a single complex number to compensate for the magnitude and the phase of each down-stream sub-channel's attenuation, and a detector decodes the resulting symbols. The multiplication with this set of complex numbers, one per down-stream sub-channel, is called frequency-domain equalization (FEQ). Figure 3 shows a block diagram of a DMT transmitter and receiver pair, assuming a noiseless channel.

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In the steady-state, the subchannel SNRs are monitored in a data-driven manner by the VTU-R during down-stream symbol periods, and the bit distribution is modified, as necessary, at the VTU-O, to optimize system performance. Upon detecting a degradation, or improvement, in one, or more, sub-channel SNRs, the VTU-R computes a modified bit distribution that better meets the desired error performance. Depending on the SNR of a degraded sub-channel, some, or all, of its bits may be moved, via a bit swap algorithm, to one, or more, other sub-channels that can support additional bits. The bit distribution change is reported to the VTU-O, where it is implemented.

For up-stream transmission, the roles of the VTU-O and VTU-R are reversed, that is, transmission and reception are performed on the up-stream set of sub-channels and the operations described above are the same.

The frequency range from zero to 11.04 MHz is partitioned into 2,048 sub-channels. The Nyquist carrier (sub-channel 2,048) and the dc carrier (sub-channel 0) are not be used for data.

Transmission may occur on up to 2047 sub-carriers, although those sub-carriers overlapping the POTS, ISDN, and amateur radio frequency bands are typically not used in the default configuration. The lowest sub-channel available to support data transmission is dependent on the POTS/ISDN splitter design.

The frame format for Zipper comprises two parts, namely:

- the DMT symbol; and
- the cyclic extension.

Orthogonality is maintained between the received signal and interfering DMT signals transmitted in the opposite direction, if they are sufficiently aligned in time. This requirement is fulfilled by the addition of a cyclic extension to the DMT symbol and the use of timing advance (TA). For the ease of description, the cyclic extension can be divided into a cyclic prefix and a cyclic suffix, where:

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- the suffix is greater than, or equal to, the propagation delay of the channel; and
- the prefix is greater than, or equal to, the guard time needed to eliminate inter-symbol interference.

5 Figure 4 illustrates the frame format for zipper.

10 When timing advance is used all transmitters commence transmission at the same time. The suffix part of the cyclic extension can be treated as an extra guard time required to maintain orthogonality between the up and down-stream channels along the wire line. To fulfil the orthogonality requirement at the receiver, the cyclic extension (prefix + suffix) must also cover the impulse response of the NEXT and the echo signal.

15 The first L_{cs} samples of the IDFT output are appended to the block of 4096 time-domain samples x_k . The last L_{cp} samples of the IDFT output are prepended to the block. The frame of samples is then read out to the digital-to-analog converter (DAC), see Figure 3, in sequence. That is, the subscripts k of the DAC samples in the sequence are $(4096-L_{cp}), \dots, 4095, 0, 1, \dots, 4094, 0, 1, \dots, (L_{cs} - 1)$.

The length of the cyclic extension (L_{cp} and L_{cs}) is typically a programmable entity set by the network operator.

20 In order to maximize high duplex efficiency, timing advance can be used so that the VTU-O transmitters and the VTU-R transmitters start transmitting each DMT frame at the same time. During the reception, a DMT symbol is only disturbed by single symbols, not affected by IFI, in the other direction due to the cyclic extension.

25 Zipper is a duplexing scheme based on the Discrete Multitone Modulation (DMT) line-coding technique and was invented at Telia Research in 1995. A patent application, SE 952775 (corresponding to WO9706619) was filed on 4th August 1995.

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In certain situations, it may be difficult for practical reasons, or it may be undesirable, to provide and maintain time synchronization between all transmitters in the same binder group. It is therefore desirable to provide an option for Zipper modems which enables them to operate in a non-synchronized mode, where only pairwise synchronization is maintained between a VTU-O and VTU-R pair. Thus, the present invention allows different users to transmit time-asynchronous DMT frames in the same binder group. However, although this is always possible, a noticeable performance penalty, due to increased NEXT, is to be expected in many situations. One possible method of mitigating this is to use pulse shaping of the DMT frames prior to transmission and additional pulse shaping in the receiver. The use of pulse shaping at the transmitter results in a higher suppression of the side lobes of the DMT signal spectrum and gives higher spectral compatibility with other systems, for example, ADSL and CAP-VDSL.

When different pairs of transceivers (modems) operating in the same binder group are asynchronous, interference from NEXT will be introduced, because, the NEXT becomes non-orthogonal and therefore degrades the performance. The reason for the NEXT becoming non-orthogonal is that the received and sampled DMT frame will include NEXT from two consecutive DMT frames which are discontinuous, as depicted in Figure 5.

In order to be able to operate in a non-synchronized mode it is necessary to suppress the NEXT by narrowing its out-of-band spectrum which interferes with the spectrum of the received signal. This can be effected by pulse shaping the DMT frame before transmission.

Pulse shaping a DMT frame is performed by forming pulse-shaped wings, e.g. from a raised cosine pulse, in the cyclic extensions of the frame as shown in Figure 6.

With pulse shaping a continuous phase is created between succeeding frames which suppresses the NEXT-interfering sub-carriers' side lobes.

The pulse shaping operation is performed at the transmitter after the cyclic extension is added to the symbol and before the digital to analog conversion (DAC) is performed. The position of the pulse shaping unit is depicted in Figure 7 which shows a block diagram of a transceiver/modem according to the present invention. The construction and operation of a VDSL modem will be immediately apparent to those skilled in the art from, Figure 7, without further explanation. However, for the sake of completeness a brief description of Figure 7 is set out below.

The transmitter arm of the modem is shown at the top of Figure 7. The input bit stream, at Rbits/s, is passed to a n-bit buffer and encoder from whence a parallel signal, $X_{d1,k}, X_{u2,k} = 0, \dots, X_{dN-1,k}, X_{uN,k} = 0$, is passed to an n-point IDFT processor. The output from the IDFT processor, $x_{1,k}, x_{2,k}, \dots, x_{N,k}$, is then passed to a unit, P/S, where the cyclic extensions, both prefix and suffix, are added to the DMT symbol. The DMT symbol with cyclic extensions then passes to a pulse shaping unit where the pulse is shaped as described above. The DMT symbol is then passed to a digital to analogue convertor and low pass filter and thence to the transmission channel.

The receiver arm of the modem is shown at the bottom of Figure 7. An incoming signal from the channel is first passed, via a low pass filter and analogue to digital convertor, to a windowing unit, see below for further details. The symbols are then passed to a stripper unit which strips off the cyclic extensions, both prefix and suffix, and thence, as a signal $y_{1,k}, y_{2,k}, \dots, y_{N,k}$, to a n-point DFT processor. The signal, $Y_{d1,k}, Y_{u2,k}, \dots, Y_{dN-1,k}, Y_{uN,k}$, is then passed to a frequency domain equaliser, FEQ, see the description of Figure 3, and thence to a decoder and n-bit buffer, as a signal $X_{d1,k}, X_{u2,k}, \dots, X_{dN-1,k}, X_{uN,k}$, which outputs the received data stream at R bits/s.

Windowing the received DMT frame, as shown in Figure 8, further suppresses the non-orthogonal NEXT. The windowing is performed by multiplying μ samples, at both the beginning and end of the $2N+\mu$ block of samples. The $\mu/2$ samples from both ends are folded and added to the $2N$ remaining block of samples at the opposite ends as shown in Figure 8. As with the pulse shaping of

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the DMT frames in the transmitter, the windowing in the receiver creates a continuous phase of the non orthogonal NEXT signals. The positioning of the windowing is shown in Figure 8.

5 Figure 9 shows the combined effect, on the non-orthogonal NEXT, of pulse shaping the DMT-frame in the transmitter and windowing the received frame in the receiver. Figure 9 also shows the signal energy, the NEXT reference signal without pulse-shaping and windowing, the NEXT signal after pulse-shaping and windowing and the FEXT signal at each sub-carrier. As seen in the figure, more than 25 dBm/Hz further suppression can be obtained by both pulse shaping and windowing.

CLAIMS

1. A telecommunications transmission system using zipper having at least two VDSL systems, each comprising a pair of zipper modems, said at least two VDSL systems belonging to a binder group common to both VDSL systems, characterised in that said telecommunications transmission system is adapted to:

- handle zipper transmissions transmitted in said binder group;
- at least partly mitigate NEXT; and
- permit transmissions in a first VDSL system which are asynchronous with transmissions in at least a second VDSL system.

2. A telecommunications system, as claimed in claim 1, characterised in that DMT frames are pulse shaped prior to transmission.

3. A telecommunications system, as claimed in claim 2, characterised in that said pulse shaping produces an improved suppression of side lobes of said DMT's spectrum.

4. A telecommunications system as claimed in either claim 2, or claim 3, characterised in that a cyclic extension is added to DMT symbols, said cyclic extension comprising:

- a suffix which is greater than, or equal to, a channel's propagation delay; and
- a prefix which is greater than, or equal to, a guard time needed to

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eliminate inter-symbol interference.

5. A telecommunications system, as claimed in claim 4, characterised in that said pulse shaping is produced by forming pulse shaped wings on a DMT frame in cyclic extensions of the DMT frame.

6. A telecommunications system, as claimed in claim 5, characterised in that said pulse shaped wings are in the form of a raised cosine pulse.

7. A telecommunications system, as claimed in any of claims 4 to 6, characterised in that said pulse shaping is performed at a transmitter, after addition of a cyclic prefix and cyclic suffix, to a symbol and prior to digital to analogue conversion.

8. A telecommunications system, as claimed in any of claims 1 to 7, characterised in that a DMT signal received by a receiver is windowed to further reduce NEXT.

9. A telecommunications system, as claimed in claim 8, characterised in that said windowing is performed by:

- multiplying μ samples at the beginning and end of a block of $2N+\mu$ samples;
- folding and adding $\mu/2$ samples from the beginning of the $2N+\mu$ block of samples to the end of the $2N$ remaining samples; and
- folding and adding $\mu/2$ samples from the end of the $2N+\mu$ block of samples to the beginning of the $2N$ remaining samples.

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10. A telecommunications system, as claimed in any previous claim, characterised in that the same number of sub-carriers are used for transmission in the up stream direction as are used for transmission in the down stream direction.

11. A telecommunications system, as claimed in any of claims 1 to 9, characterised in that a different number of sub-carriers are used for transmission in the up stream and down stream directions.

12. In a telecommunications transmission system using zipper and having at least two VDSL systems, each comprising a pair of zipper modems, said at least two VDSL systems belonging to a single binder group common to both VDSL systems, a method of transmission characterised by permitting zipper transmissions of said first and, at least, said second VDSL transmission systems, to be transmitted in said single binder group, where transmission in said first VDSL system are asynchronous with zipper transmission in said second VDSL system and in which the effects of NEXT are, at least partly, mitigated.

13. A method, as claimed in claim 12, characterised by pulse shaping DMT frames prior to transmission.

14. A method, as claimed in claim 13, characterised by said pulse shaping producing an improved suppression of side lobes of said DMT's spectrum.

15. A method, as claimed in either claim 13, or claim 14, characterised by adding a cyclic extension to DMT symbols, said cyclic extension comprising:

- a suffix which is greater than, or equal to, a channel's propagation delay; and
- a prefix which is greater than, or equal to, a guard time needed to eliminate inter-symbol interference.

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16. A method as claimed in claim 15, characterised by forming pulse shaped wings on a DMT frame in the cyclic extensions of the DMT frame.

17. A method as claimed in claim 16, characterised by forming said pulse shaped wings as a raised cosine pulse.

18. A method as claimed in any of claims 14 to 17, characterised by performing said pulse shaping at a transmitter after addition of a cyclic extension to a symbol and prior to digital to analogue conversion.

19. A method as claimed in any of claims 12 to 18, characterised by windowing a DMT signal received by a receiver to further reduce NEXT.

20. A method as claimed in claim 19, characterised by using the following steps to perform said windowing:

- multiplying μ samples at the beginning and end of a block of $2N+\mu$ samples;
- folding and adding $\mu/2$ samples from the beginning of the $2N+\mu$ block of samples to the end of the $2N$ remaining samples; and
- folding and adding $\mu/2$ samples from the end of the $2N+\mu$ block of samples to the beginning of the $2N$ remaining samples.

21. A method as claimed in any of claims 12 to 20, characterised by transmitting the same number of sub-carriers in both an upstream and down stream direction.

22. A method, as claimed in any of claims 12 to 20, characterised by transmitting a different number of sub-carriers in the up stream and down stream

directions.

23. A transmitter, for use in a transmission system as claimed in any of claims 1 to 11, characterised in that said transmitter comprises an a b-bit buffer and encoder for receiving an input bit stream at a rate of R bit/s, a n-point IDFT processor for receiving an output from said b-bit buffer and encoder, extension means for adding a cyclic extension to an output of said IDFT processor, a pulse shaper for shaping a DMT symbol output from said extension means, and a digital to analogue converter and low pass filter for converting a DMT symbol received from said pulse shaper to analogue form and passing said DMT to a transmission channel.

24. A transmitter, as claimed in claim 23, characterised in that said pulse shaper forms pulse shaped wings on said DMT symbol as raised cosine pulses.

25. A receiver, for use in a transmission system as claimed in any of claims 1 to 11, characterised in that said receiver includes an analogue to digital converter for digitising a DMT symbol received from a transmission channel, a windowing unit connected to an output of said analogue to digital converter, a stripper unit for removing cyclic extensions to said DMT symbol, an n-point DFT processor for receiving an output from said stripper unit, a frequency domain equalisation unit for receiving an output from said n-point DFT unit and decoder, and a b-bit buffer for receiving an output from said frequency domain equalisation unit and outputting a bit stream at R bit/s.

26. A receiver, as claimed in claim 25, characterised in that said windowing unit performs the following operations:

- multiplying μ samples at the beginning and end of a block of $2N+\mu$ samples;
- folding and adding $\mu/2$ samples from the beginning of the $2N+\mu$ block of samples to the end of the $2N$ remaining samples; and

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- folding and adding $\mu/2$ samples from the end of the $2N+\mu$ block of samples to the beginning of the $2N$ remaining samples.

27. A modem, for use in a transmission system as claimed in any of claims 1 to 11, characterised in that said modem includes a transmitter as claimed in either claim 23, or 24.

28. A modem, as claimed in claim 27, characterised in that said modem includes a receiver, as claimed in either claim 25, or 26.

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ABSTRACT

Improvements In, or Relating to, VDSL

Zipper is a time-synchronized frequency-division duplex implementation of discrete multi tone (DMT) modulation. Two communicating Zipper modems transmit DMT symbols simultaneously with a common clock. The Zipper scheme implies that every carrier, in the total set of carriers in the DMT signal, is exclusively chosen to be used for either the up-stream or the down-stream direction. When all transmitters are time synchronized, the near end cross-talk (NEXT) and near end echoes injected into the received signal are orthogonal to the desired signal.

The present invention provides a telecommunications transmission system using zipper and having at least two VDSL systems. Each of VDSL system comprises a pair of zipper modems communicating over a cable transmission path. The two VDSL systems employ a common cable. The telecommunications transmission system is adapted to:

- handle zipper transmissions transmitted over the common cable;
- at least partly mitigate NEXT; and
- permit transmissions in a first VDSL system which are asynchronous with transmissions in a second VDSL system.

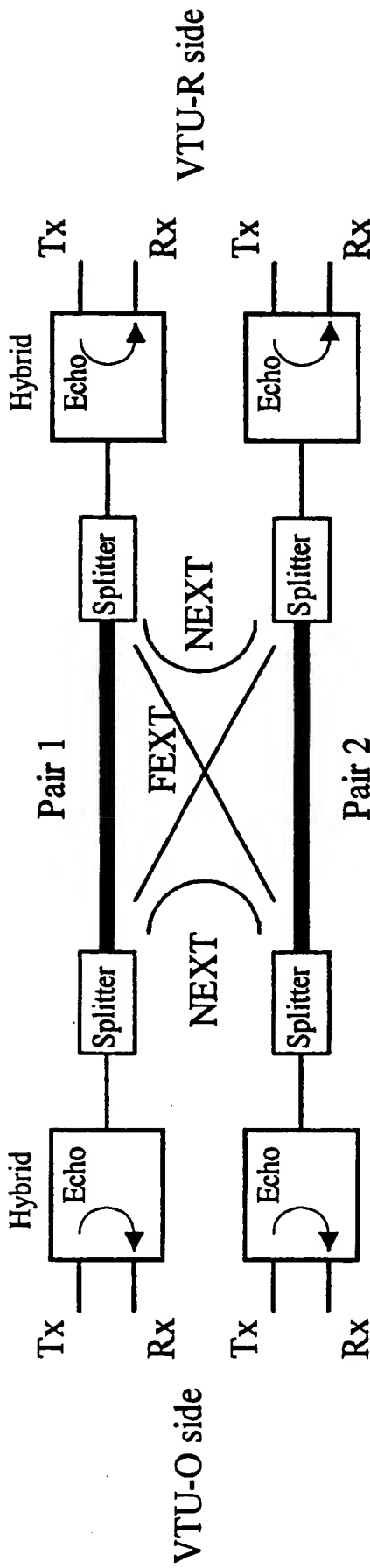


FIGURE 1

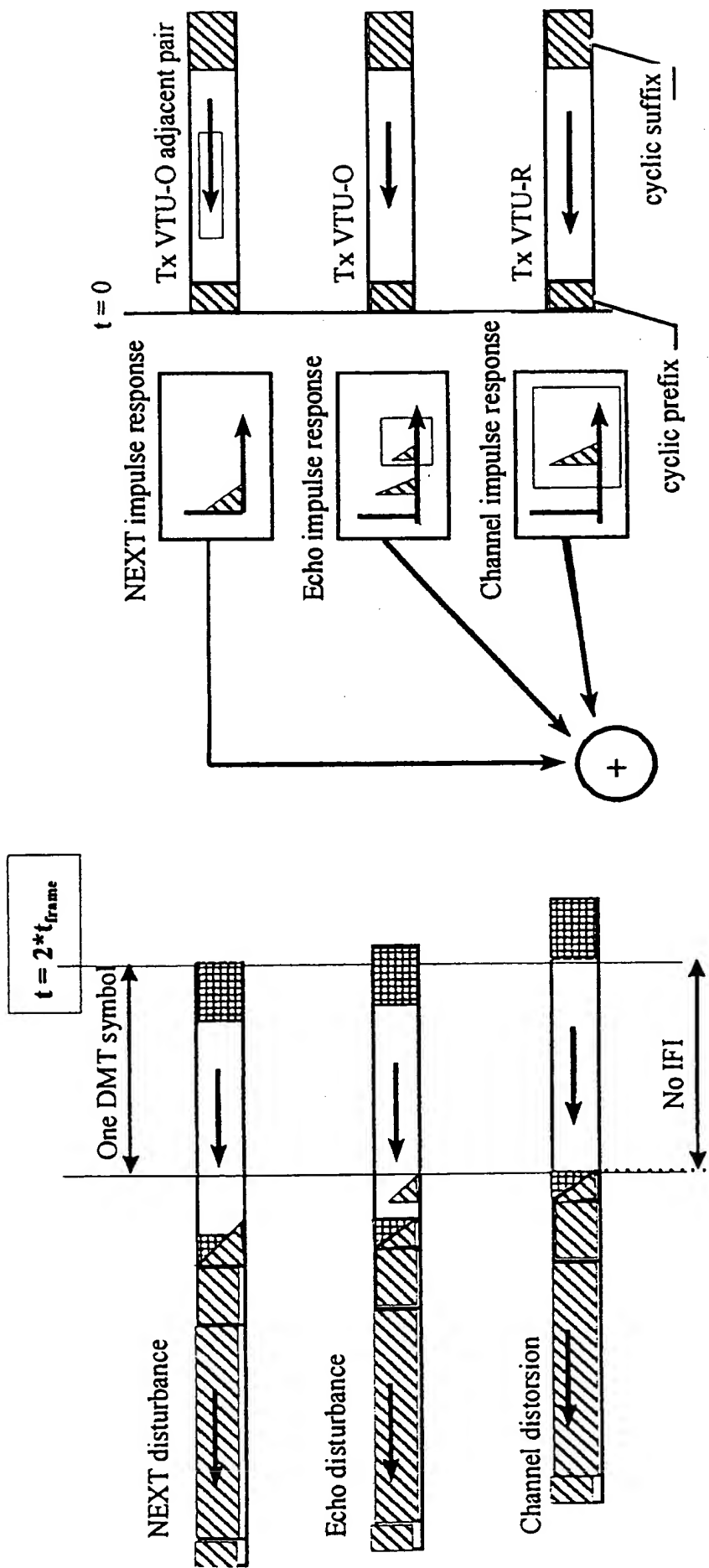


FIGURE 2

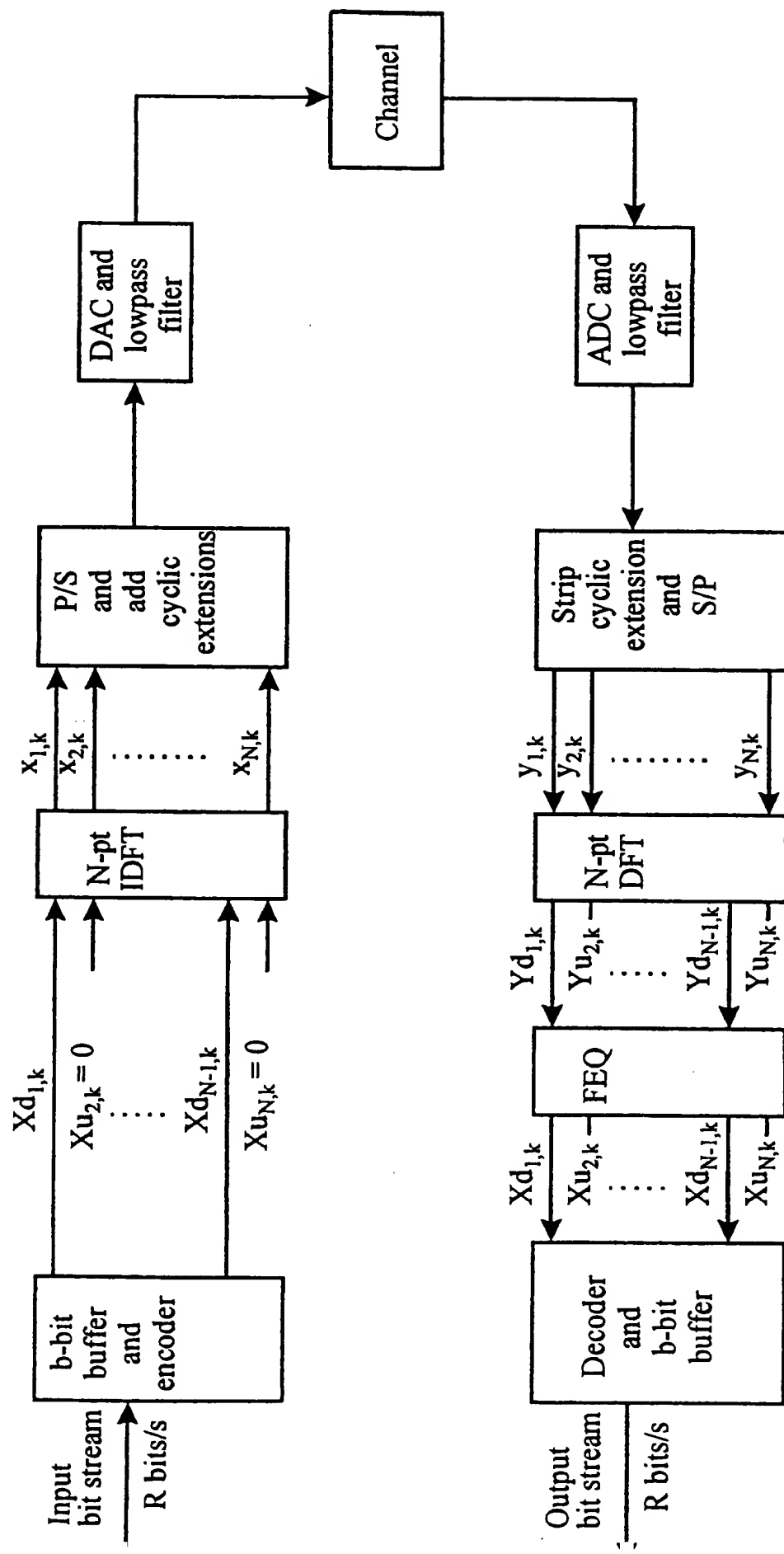


FIGURE 3

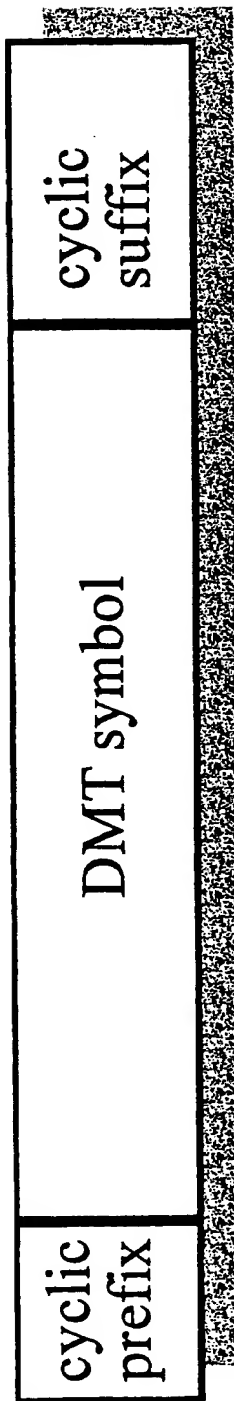


FIGURE 4

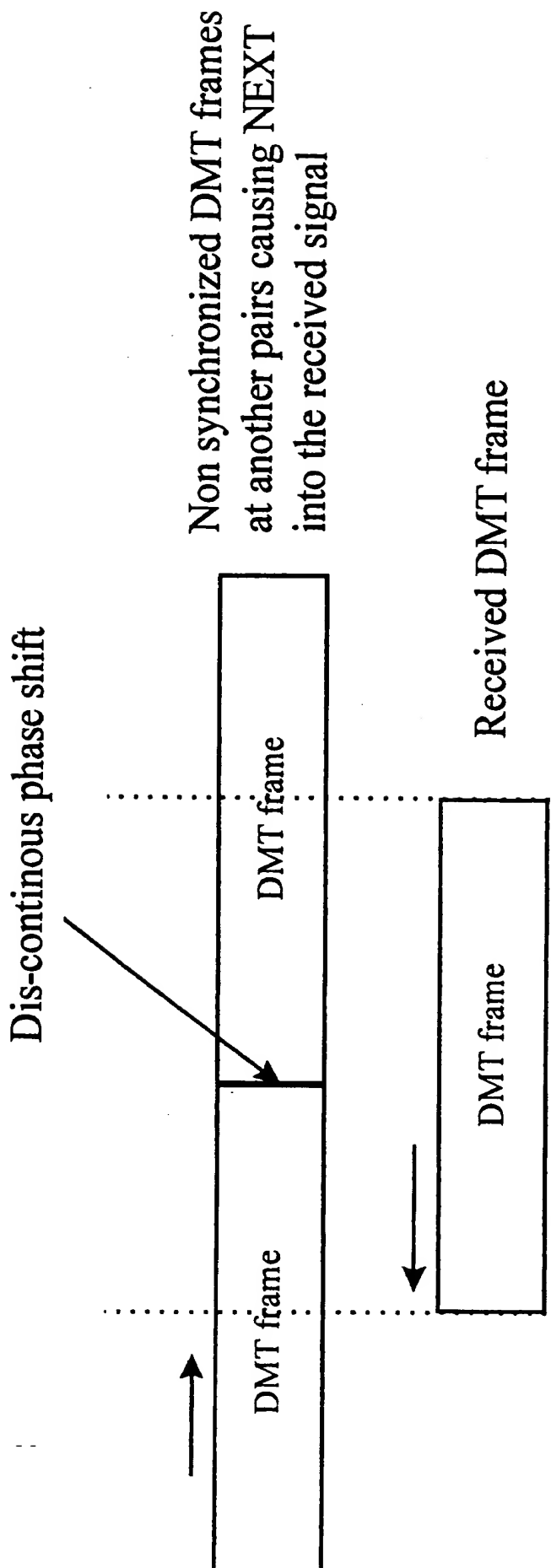


FIGURE 5

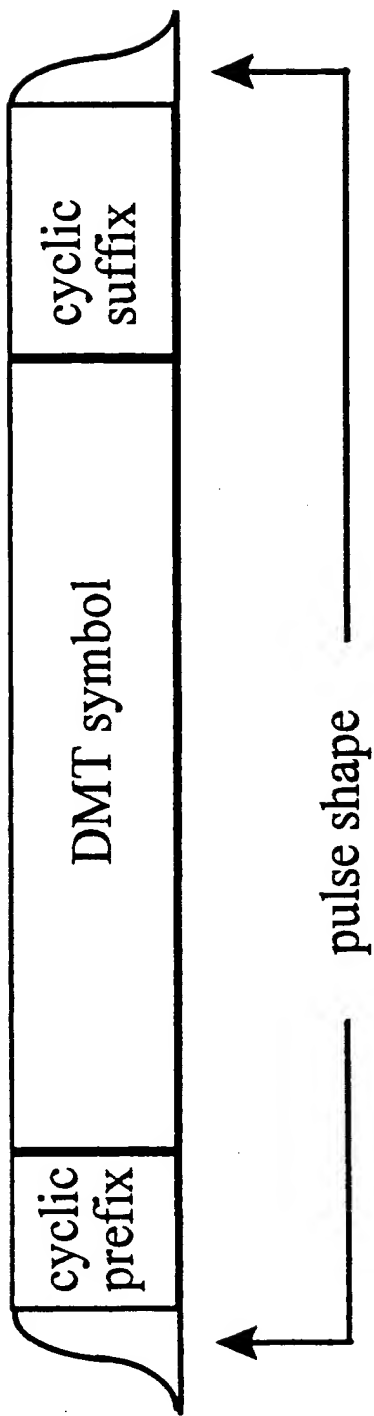


FIGURE 6

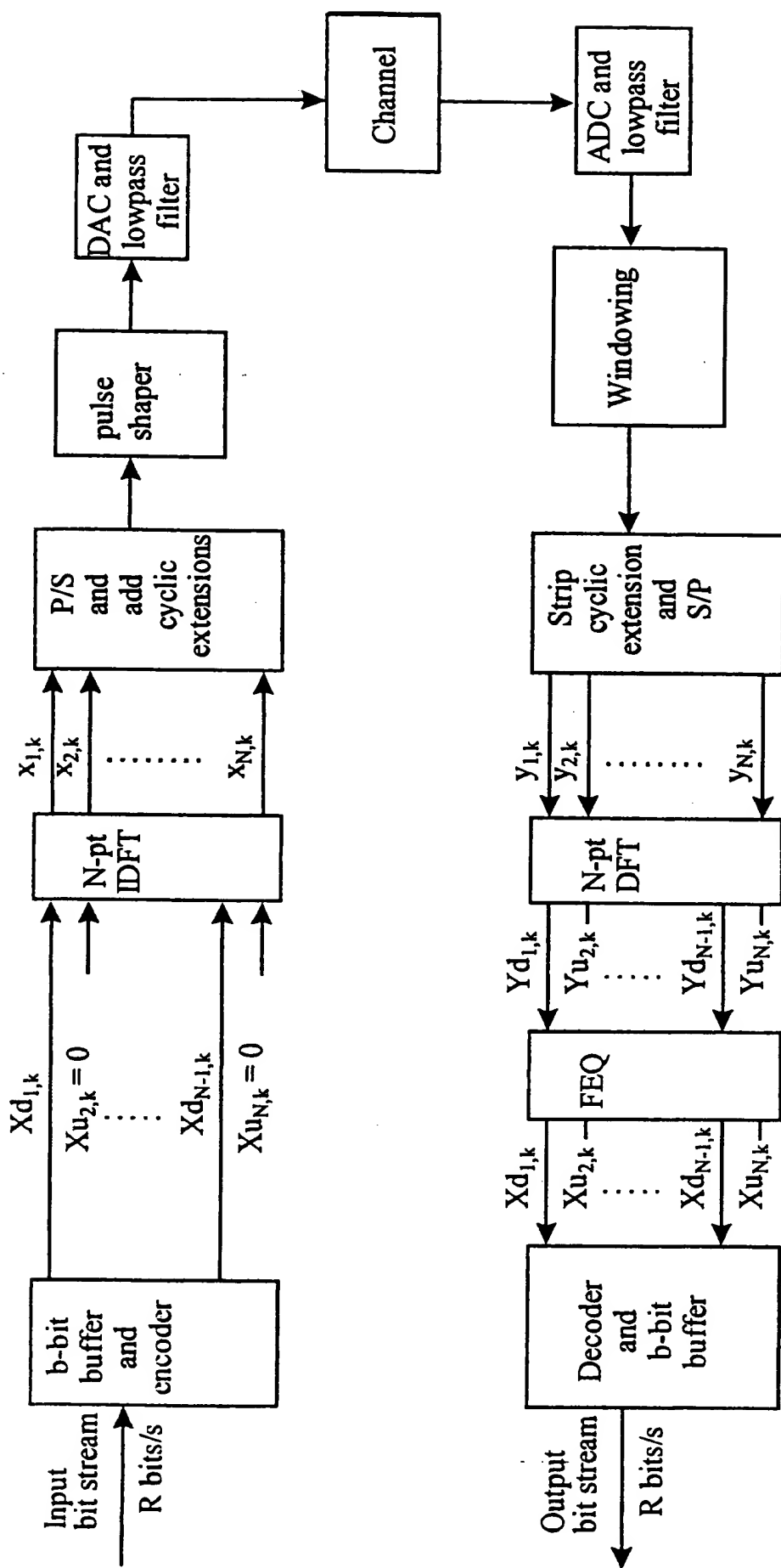


FIGURE 7

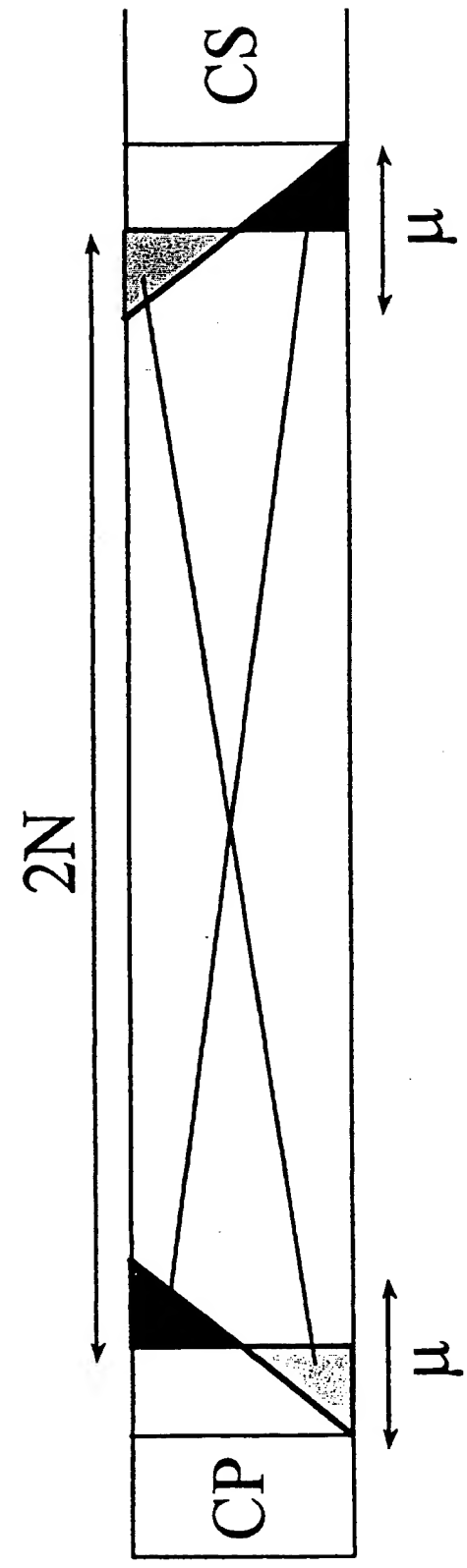


FIGURE 8

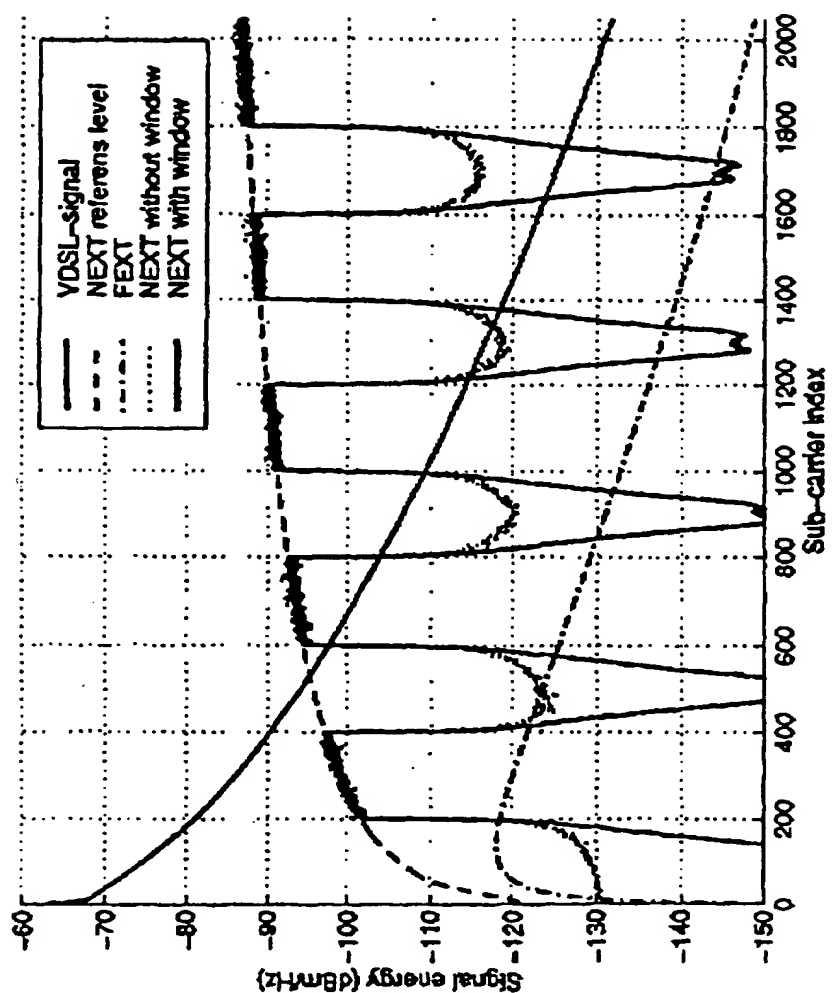


FIGURE 9